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- [#5](#) ((static schedule and trace)<in>metadata)
- [#6](#) ((static schedule and instruction and pipeline?)<in>metadata)
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IEEE JNL IEEE Journal or Magazine

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IEEE CNF IEEE Conference Proceeding

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- ☐
- 1. Dynamic instruction scheduling and the Astronautics ZS-1**
Smith, J.E.;
Computer
Volume 22, Issue 7, July 1989 Page(s):21 - 35
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MagazineIEEE IEEE Conference
CNF ProceedingIEEE CNF IEE Conference
Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Load distribution via static scheduling and client redirection for replicated Web s**
Wenting Tang; Mutka, M.W.;
Parallel Processing, 2000. Proceedings. 2000 International Workshops on
21-24 Aug. 2000 Page(s):127 - 133
[AbstractPlus](#) | Full Text: [PDF](#)(608 KB) IEEE CNF

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Search history:

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1	INZZ	static ADJ schedule AND trace	unrestricted	0	-

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- ☐ Classification codes A: Physics, 8
- ☐ Classification codes A: Physics, 9
- ☐ Classification codes B: Electrical & Electronics, 0-5
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<input type="checkbox"/>	L11	L8 and trace and instruction?	10
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<input type="checkbox"/>	L9	L8 and (tracing near5 instruction?)	0
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<input type="checkbox"/>	L7	L6 and static schedule	0
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Search Results - Record(s) 1 through 1 of 1 returned.

☐ 1. Document ID: US 5802585 A

L5: Entry 1 of 1

File: USPT

Sep 1, 1998

US-PAT-NO: 5802585

DOCUMENT-IDENTIFIER: US 5802585 A

TITLE: Batched checking of shared memory accesses

DATE-ISSUED: September 1, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Scales; Daniel J.	Palo Alto	CA		
Thekkath; Chandramohan A.	Palo Alto	CA		

US-CL-CURRENT: 711/154; 711/147, 717/158

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	NAME	Draw. De
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Term	Documents
TRACING	34057
TRACINGS	3039
(1 AND TRACING).PGPB,USPT.	1
(L1 AND TRACING).PGPB,USPT.	1

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Search Results - Record(s) 1 through 10 of 10 returned.

☐ 1. Document ID: US 6106573 A

L6: Entry 1 of 10

File: USPT

Aug 22, 2000

US-PAT-NO: 6106573

DOCUMENT-IDENTIFIER: US 6106573 A

**** See image for Certificate of Correction ****

TITLE: Apparatus and method for tracing microprocessor instructions

DATE-ISSUED: August 22, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Mahalingaiah; Rupaka	Austin	TX		
Pickett; James K.	Austin	TX		

US-CL-CURRENT: 717/128; 712/204, 712/23, 712/3

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KMC	Draw D
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☐ 2. Document ID: US 5933626 A

L6: Entry 2 of 10

File: USPT

Aug 3, 1999

US-PAT-NO: 5933626

DOCUMENT-IDENTIFIER: US 5933626 A

**** See image for Certificate of Correction ****

TITLE: Apparatus and method for tracing microprocessor instructions

DATE-ISSUED: August 3, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Mahalingaiah; Rupaka	Austin	TX		
Pickett; James K.	Austin	TX		

US-CL-CURRENT: 712/227; 714/38

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KMC	Draw D
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☐ 3. Document ID: US 5922070 A

L6: Entry 3 of 10

File: USPT

Jul 13, 1999

US-PAT-NO: 5922070

DOCUMENT-IDENTIFIER: US 5922070 A

TITLE: Pipelined data processing including program counter recycling

DATE-ISSUED: July 13, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Swoboda; Gary L.	Sugar Land	TX		
Hammes; Mark R.	Houston	TX		
Deao; Douglas	Brookshire	TX		
Balmer; Keith	Bedford			GB
Ing-Simmons; Nick	Huntingdon			GB

US-CL-CURRENT: 712/244; 712/227

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KNOW	Drawn De
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☐ 4. Document ID: US 5724566 A

L6: Entry 4 of 10

File: USPT

Mar 3, 1998

US-PAT-NO: 5724566

DOCUMENT-IDENTIFIER: US 5724566 A

TITLE: Pipelined data processing including interrupts

DATE-ISSUED: March 3, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Swoboda; Gary L.	Sugar Land	TX		
Hammes; Mark R.	Houston	TX		
Deao; Douglas	Brookshire	TX		
Balmer; Keith	Bedford			GB2
Ing-Simmons; Nick	Huntingdon			GB2

US-CL-CURRENT: 712/245; 712/205

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KNOW	Drawn De
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☐ 5. Document ID: US 5594864 A

L6: Entry 5 of 10

File: USPT

Jan 14, 1997

US-PAT-NO: 5594864

DOCUMENT-IDENTIFIER: US 5594864 A

**** See image for Certificate of Correction ****

TITLE: Method and apparatus for unobtrusively monitoring processor states and characterizing bottlenecks in a pipelined processor executing grouped instructions

DATE-ISSUED: January 14, 1997

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Trauben; Richard D.	Morgan Hill	CA		

US-CL-CURRENT: 714/39; 712/227, 714/30, 714/31, 714/38

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KMIC	Draw. De
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☐ 6. Document ID: US 5574937 A

L6: Entry 6 of 10

File: USPT

Nov 12, 1996

US-PAT-NO: 5574937

DOCUMENT-IDENTIFIER: US 5574937 A

**** See image for Certificate of Correction ****

TITLE: Method and apparatus for improving instruction tracing operations in a computer system

DATE-ISSUED: November 12, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Narain; Bikas	Gilbert	AZ		

US-CL-CURRENT: 712/23; 712/200, 714/45

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KMIC	Draw. De
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☐ 7. Document ID: US 5564028 A

L6: Entry 7 of 10

File: USPT

Oct 8, 1996

US-PAT-NO: 5564028

DOCUMENT-IDENTIFIER: US 5564028 A

TITLE: Pipelined data processing including instruction trace

DATE-ISSUED: October 8, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
------	------	-------	----------	---------

Swoboda; Gary L.	Sugar Land	TX
Hammes; Mark R.	Houston	TX
Deao; Douglas	Brookshire	TX

US-CL-CURRENT: 712/227; 710/264, 714/45

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	RMK	Draw D
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☐ 8. Document ID: US 5438670 A

L6: Entry 8 of 10

File: USPT

Aug 1, 1995

US-PAT-NO: 5438670

DOCUMENT-IDENTIFIER: US 5438670 A

**** See image for Certificate of Correction ****

TITLE: Method of prechecking the validity of a write access request

DATE-ISSUED: August 1, 1995

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Baror; Gigi	Austin	TX		
Beck; Moti	Tel Aviv			IL
Biran; Dan	Tel Aviv			IL
Cohen; Elliot	Tel Aviv			IL
Hadas; Yair	Tel Aviv			IL
Konstantin; Benny	Kfar Saba			IL
Levy; Jonanthan	Kfar Saba			IL
Marko; Reuven	Netanya			IL
Ostrer; Aharon	Tel Aviv			IL
Saban; Rami	Holon			IL
Shackam; Alon	Tel Aviv			IL
Shahar; Boaz	Nof Yam			IL

US-CL-CURRENT: 711/3; 711/169

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	RMK	Draw D
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☐ 9. Document ID: US 4945479 A

L6: Entry 9 of 10

File: USPT

Jul 31, 1990

US-PAT-NO: 4945479

DOCUMENT-IDENTIFIER: US 4945479 A

TITLE: Tightly coupled scientific processing system

DATE-ISSUED: July 31, 1990

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Rusterholz; John T.	Roseville	MN		
Homan; Charles J.	St. Paul	MN		
Brown; Lowell E.	Anoka	MN		
Bennett; Donald B.	Burnsville	MN		
Malnati; Robert J.	St. Paul	MN		
Hamstra; James R.	Plymouth	MN		

US-CL-CURRENT: 712/3

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw D
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☐ 10. Document ID: US 4873630 A

L6: Entry 10 of 10

File: USPT

Oct 10, 1989

US-PAT-NO: 4873630

DOCUMENT-IDENTIFIER: US 4873630 A

TITLE: Scientific processor to support a host processor referencing common memory

DATE-ISSUED: October 10, 1989

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Rusterholz; John T.	Roseville	MN		
Lahti; Archie E.	Fridley	MN		
Bushard; Louis B.	Anoka	MN		
Byers; Larry L.	Apple Valley	MN		
Hamstra; James R.	Plymouth	MN		
Homan; Charles J.	St. Paul	MN		

US-CL-CURRENT: 712/3; 712/5, 712/6, 712/9

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw D
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Term	Documents
TRACING	34057
TRACINGS	3039
INSTRUCTION	176048
INSTRUCTIONS	318095
INSTRUCTION?	0
INSTRUCTIONA	2

INSTRUCTIONB	1
INSTRUCTIONG	5
INSTRUCTIONI	1
INSTRUCTIONJ	1
INSTRUCTIONL	2
((TRACING NEAR5 INSTRUCTION?) AND INSTRUCTION PIPELINE?).PGPB,USPT.	10

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☐ 1. Document ID: US 20050055675 A1

L11: Entry 1 of 10

File: PGPB

Mar 10, 2005

PGPUB-DOCUMENT-NUMBER: 20050055675

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050055675 A1

TITLE: Generation of software objects from a hardware description

PUBLICATION-DATE: March 10, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Neifert, William	Arlington	MA	US	
Marantz, Joshua	Brookline	MA	US	
Sayde, Richard	Carlisle	MA	US	
Tatham, Joseph	Arlington	MA	US	
Lehotsky, Alan	Carlisle	MA	US	
Ladd, Andrew	Maynard	MA	US	
Seneski, Mark	Roslindale	MA	US	
Atkins, Aron	Arlington	MA	US	

US-CL-CURRENT: [717/135](#); [703/22](#), [717/138](#), [717/141](#), [717/154](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	RISC	Draw D
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☐ 2. Document ID: US 20040122644 A1

L11: Entry 2 of 10

File: PGPB

Jun 24, 2004

PGPUB-DOCUMENT-NUMBER: 20040122644

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040122644 A1

TITLE: Optimized execution of software objects generated from a hardware description

PUBLICATION-DATE: June 24, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
------	------	-------	---------	---------

Neifert, William	Arlington	MA	US
Marantz, Joshua	Brookline	MA	US
Sayde, Richard	Carlisle	MA	US
Tatham, Joseph	Arlington	MA	US
Lehotsky, Alan	Carlisle	MA	US
Ladd, Andrew	Maynard	MA	US
Seneski, Mark	Roslindale	MA	US
Atkins, Aron	Arlington	MA	US

US-CL-CURRENT: 703/16

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw Da
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☐ 3. Document ID: US 20040117168 A1

L11: Entry 3 of 10

File: PGPB

Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117168
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117168 A1

TITLE: Global analysis of software objects generated from a hardware description

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Neifert, William	Arlington	MA	US	
Marantz, Joshua	Brookline	MA	US	
Sayde, Richard	Carlisle	MA	US	
Tatham, Joseph	Arlington	MA	US	
Lehotsky, Alan	Carlisle	MA	US	
Ladd, Andrew	Maynard	MA	US	
Seneski, Mark	Roslindale	MA	US	
Atkins, Aron	Arlington	MA	US	

US-CL-CURRENT: 703/14

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw Da
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☐ 4. Document ID: US 20040117167 A1

L11: Entry 4 of 10

File: PGPB

Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117167
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117167 A1

TITLE: Simulation of software objects generated from a hardware description

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Neifert, William	Arlington	MA	US	
Marantz, Joshua	Brookline	MA	US	
Sayde, Richard	Carlisle	MA	US	
Tatham, Joseph	Arlington	MA	US	
Lehotsky, Alan	Carlisle	MA	US	
Ladd, Andrew	Maynard	MA	US	
Seneski, Mark	Roslindale	MA	US	
Atkins, Aron	Arlington	MA	US	

US-CL-CURRENT: 703/14

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWAC	Draw D
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☐ 5. Document ID: US 20030139917 A1

L11: Entry 5 of 10

File: PGPB

Jul 24, 2003

PGPUB-DOCUMENT-NUMBER: 20030139917

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030139917 A1

TITLE: Late binding of resource allocation in a performance simulation infrastructure

PUBLICATION-DATE: July 24, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Hardwick, Jonathan Christopher	Cambridge		GB	
Papaefstahiou, Efstathios	Cambridge		GB	

US-CL-CURRENT: 703/21

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWAC	Draw D
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☐ 6. Document ID: US 20030121027 A1

L11: Entry 6 of 10

File: PGPB

Jun 26, 2003

PGPUB-DOCUMENT-NUMBER: 20030121027

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030121027 A1

TITLE: Behavioral abstractions for debugging coordination-centric software designs

PUBLICATION-DATE: June 26, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Hines, Kenneth J.	Bothell	WA	US	

US-CL-CURRENT: 717/135; 717/113, 717/120, 719/318, 719/321

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWAC	Draw D
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☐ 7. Document ID: US 20030028858 A1

L11: Entry 7 of 10

File: PGPB

Feb 6, 2003

PGPUB-DOCUMENT-NUMBER: 20030028858

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030028858 A1

TITLE: Evolution diagrams for debugging distributed embedded software applications

PUBLICATION-DATE: February 6, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Hines, Kenneth J.	Bothell	WA	US	

US-CL-CURRENT: 717/125; 717/130, 717/131

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWAC	Draw D
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☐ 8. Document ID: US 20020174415 A1

L11: Entry 8 of 10

File: PGPB

Nov 21, 2002

PGPUB-DOCUMENT-NUMBER: 20020174415

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020174415 A1

TITLE: System and method for debugging distributed software environments

PUBLICATION-DATE: November 21, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Hines, Kenneth J.	Bothell	WA	US	

US-CL-CURRENT: 717/127

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWAC	Draw D
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☐ 9. Document ID: US 20020087953 A1

L11: Entry 9 of 10

File: PGPB

Jul 4, 2002

PGPUB-DOCUMENT-NUMBER: 20020087953

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020087953 A1

TITLE: Data structure and method for detecting constraint conflicts in
coordination-centric software systems

PUBLICATION-DATE: July 4, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Hines, Kenneth J.	Bothell	WA	US	

US-CL-CURRENT: 717/125

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWAC	Draw D
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☐ 10. Document ID: US 20020062463 A1

L11: Entry 10 of 10

File: PGPB

May 23, 2002

PGPUB-DOCUMENT-NUMBER: 20020062463

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020062463 A1

TITLE: Dynamic control graphs for analysis of coordination-centric software designs

PUBLICATION-DATE: May 23, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Hines, Kenneth J.	Bothell	WA	US	

US-CL-CURRENT: 714/38

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWAC	Draw D
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Term	Documents
TRACE	161110
TRACES	101018
INSTRUCTION?	0
INSTRUCTIONA	2
INSTRUCTIONB	1

INSTRUCTIONG	5
INSTRUCTIONI	1
INSTRUCTIONJ	1
INSTRUCTIONL	2
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